

Publications List

Journals

1. S. D'Amico and A. Baschiroto "A compact High-Frequency Low-Power Continuous-Time Gm-C Biquad Cell" IEE Electronics Letters, 29th May 2003, vol. 39, no. 11, pages 821-822 (DOI:10.1049/el:20030537).
2. G. Maruccio, P. Visconti, V. Arima, S. D'Amico, Adriana Biasco, E. D'Amone, R. Cingolani and R. Rinaldi "Field Effect Transistor based on a modified DNA base" Nanoletters, pages 479-483, vol. 3, no.4, April 2003 (DOI: 10.1021/nl034046c).
3. G. Maruccio, P. Visconti, S. D'Amico, E. D'Amone, R. Rinaldi, R. Cingolani "Planar nanotips as probes for transport experiments in molecules" Microelectronic Engineering, pages 838-844, vol.67-68, 2003 (DOI: 10.1016/S0167-9317(03)00145-X).
4. S. D'Amico, G. Maruccio, P. Visconti, E. D'Amone, R. Cingolani and R. Rinaldi; S. Masiero, G.P. Spada and G. Gottarelli "Transistors based on the Guanosine Molecule (a DNA base)" Microelectronics Journal, pages 961-963, vol. 34, no. 10, October 2003 (DOI: 10.1016/S0026-2692(03)00197-6).
5. S. D'Amico, G. Maruccio, P. Visconti, E. D'Amone, A. Bramanti, R. Cingolani, R. Rinaldi "Ambipolar transistors based on azurin proteins" IEE Proceedings on Nanobiotechnology, pages 173-175, vol. 151, no. 5, October 2005 (DOI: 10.1049/ip-nbt:20041032).
6. Stefano D'Amico, A. Baschiroto "Active-Gm-RC continuous-time biquadratic cells" Analog Integrated Circuits and Signal Processing, November 2005, vol. 45, no. 3, pages 281-294 (DOI: 10.1007/s10470-005-4956-9).
7. S. D'Amico, V. Giannini and A. Baschiroto "A Low-Power Reconfigurable Analog Filter for UMTS/WLAN Receivers" Analog Integrated Circuits and Signal Processing, January 2006, vol. 46, no. 1, pages 65-72 (DOI: 10.1007/s10470-005-4586-2).
8. S. D'Amico, V. Giannini and A. Baschiroto "A 4th-order Active-Gm-RC reconfigurable (UMTS/WLAN) filter" IEEE Journal of Solid-State Circuits, pages 1630-1636, vol. 46, no.1, July 2006 (DOI: 10.1109/JSSC.2006.873676).
9. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, and A. Baschiroto "1.2-V Low-Power Multi-Mode DAC+Filter Blocks for Reconfigurable (WLAN/UMTS, WLAN/Bluetooth) Transmitters" IEEE Journal of Solid-State Circuits, pages 1970-1982, vol. 41, no. 9, September 2006 (DOI: 10.1109/JSSC.2006.880602).
10. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschiroto "A 1.2V, 30.4dBm OIP3 Reconfigurable Analog Baseband Channel for UMTS/WLAN Transmitters" IEEE Transactions on Circuits and Systems I, pages 2125-2130 vol. 53, no. 10, October 2006 (DOI: 10.1109/TCSI.2006.883174).
11. S. D'Amico, M. Conta and A. Baschiroto "A 4.1-mW 10-MHz Fourth-Order Source-Follower-Based Continuous-Time filter with 79dB DR" IEEE Journal of Solid-State Circuits, pages 2713-2719, vol. 41 no. 12, December 2006 (DOI: 10.1109/JSSC.2006.884191).
12. Bramanti, G. Maruccio, P. Visconti, S. D'Amico, R. Cingolani, R. Rinaldi "Field Emission Breakdown and Electromigration in Insulated Planar Nanoscopic Contacts" IEEE Transactions on Electron Devices, pages 2958-2964, vol. 53, no. 12, December 2006 (DOI:10.1109/TED.2006.885659).
13. F. Corsi, G. Matarrese, C. Marzocca, A. Dragone, A. Baschiroto, S. D'Amico "Tuning of high-speed telecommunication filters, via I/O cross-correlation evaluation", IEEE Transactions on Circuits and Systems I, pages 329-337, vol. 54, no. 2, February 2007 (DOI: 10.1109/TCSI.2006.886005).
14. J. Ryckaert, M. Verhelst, M. Badaroglu, S. D'Amico, V. De Heyn, C. Desset, P. Nuzzo, B. Van Poucke, P. Wambacq, A. Baschiroto, W. Dehaene, G. Van der Plas "A CMOS Ultra-Wideband Receiver for Low Data-Rate Communication" IEEE Journal of Solid-State Circuits, pages 2515 - 2527, vol. 42. no. 11, November 2007 (DOI: 10.1109/JSSC.2007.907195).
15. V. Giannini, J. Craninckx, J. Compiet, B. Come, S. D'Amico, A. Baschiroto "Flexible baseband analog circuits for Software-Defined Radio Front-Ends" IEEE Journal of Solid-State Circuits, pages 1501-1511, vol. 42, no. 7, July 2007 (DOI: 10.1109/JSSC.2007.899103).
16. S. D'Amico, A. Baschiroto, M. De Matteis, N. Ghittori, A. Vigna, P. Malcovati "A CMOS $5\text{ nV}/\sqrt{\text{Hz}}$ 74-dB-Gain-Range 82-dB-DR Multistandard Baseband Chain for Bluetooth, UMTS, and WLAN" IEEE Journal of Solid-State Circuits, pages 1534-1541, vol. 43, no. 7, July 2008 (DOI: 10.1109/JSSC.2008.922378).
17. S. D'Amico, M. De Matteis, A. Baschiroto "A 6.4 mW, $4.9\text{ nV}/\sqrt{\text{Hz}}$, 24 dBm IIP3 VGA for a multi-standard (WLAN, UMTS, and Bluetooth) receiver" Analog Integrated Circuits and Signal Processing, vol. 61, no. 1 pages 1-7, October 2009 (DOI: 10.1007/s10470-008-9269-3).
18. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschiroto "An IEEE 802.11 and 802.16 WLAN Wireless Transmitter Baseband Architecture with a 1.2-V, 600-MS/s, 2.4-mW DAC", Analog Integrated Circuits and Signal Processing, pages 231-242, vol. 59, no. 3, June 2009 (DOI: 10.1007/s10470-008-9262-x).

19. M. De Matteis, S. D'Amico, A. Baschiroto "A 0.55 V 60 dB-DR Fourth-Order Analog Baseband Filter" *Journal of Solid-State Circuits*, pages 2525-2534, vol. 44, no. 9, September 2009 (DOI: 10.1109/JSSC.2009.2024801).
20. A. Baschiroto, G. Chiodini, P. Creti, S. D'Amico, M. De Matteis, F. Grancagnolo, M. Panareo, S. Spagnolo, G. Tassielli "Cluster counting drift chamber as high precision tracker for ILC experiments" *Nuclear Instruments & Methods in Physics Research. Section A, Accelerators, Spectrometers, Detectors, and Associated Equipments*, vol. 598, issue 1, 1 January 2009, pages 98-101 (DOI: 10.1016/j.nima.2008.08.073).
21. S. D'Amico, G. Cocciolo, M. De Matteis, A. Baschiroto "A 6.9 mA 5 bits 90nm 1GS/s ADC without calibration for UWB application" *Microelectronics Journal* vol. 42, no. 2, pages 325–329, February 2011 (DOI: 10.1016/j.mejo.2010.11.006).
22. M. De Blasi, P. Delizia, S. D'Amico, A. Baschiroto "A 16 bit 20 kHz bandwidth discrete-time RD modulator with VCO-based quantizer" *Analog Integrated Circuits and Signal Processing*, June 2011 pages 1-9 (DOI: 10.1007/s10470-011-9664-z).
23. S. D'Amico, M. De Blasi, M. De Matteis, A. Baschiroto "A 255 MHz Programmable Gain Amplifier and Low-Pass Filter for Ultra Low Power Impulse-Radio UWB Receivers" *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 59, issue 2, February 2012, pages: 337 – 345 (DOI: 10.1109/TCSI.2011.2163886).
24. P. Visconti, S. D'Amico, A. Baschiroto, D. Romanello, P. Costantini, V. Ventura, G. Cavallera "Bidirectional communication system on power line integrated on electronic board for driving of LED and HID lamps" *Advances in Power Electronics*, vol. 2012, Article ID 872383, 10 pages (DOI:10.1155/2012/872383).
25. V. Chironi, B. Debaillie, S. D'Amico, A. Baschiroto, J. Craninckx, M. Ingels "A Digitally Modulated Class-E Polar Amplifier in 90nm CMOS" *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 60, no. 4, April 2013, pages 918-925 (DOI: 10.1109/TCSI.2012.2215398).
26. Y. Chen, P.I. Mak, S. D'Amico, L. Zhang, H. Qian, Y. Wang "A Single-Branch Third-Order Pole–Zero Low-Pass Filter With 0.014-mm² Die Size and 0.8-kHz (1.25-nW) to 0.94-GHz (3.99-mW) Bandwidth–Power Scalability" *IEEE Transactions on Circuits and Systems—II: Express Briefs*, vol. 60, no. 11, November 2013 pages 761-765 (DOI: 10.1109/TCSII.2013.2281717).
27. S. D'Amico, G. Cocciolo, A. Spagnolo, M. De Matteis, A. Baschiroto "A 7.65-mW 5-bit 90-nm 1-Gs/s Folded Interpolated ADC Without Calibration" *IEEE Transactions on Instrumentation and Measurement*, February 2014, vol. 23, no. 2, pages 295-303 (DOI: 10.1109/TIM.2013.2278998).
28. A. Donno, S. D'Amico, M. De Matteis, A. Baschiroto "A 10-b 50-MSPS Low Power Pipeline ADC for Ultra-High Energy Cosmic Rays Detection" *IEEE Transactions on Nuclear Science*, vol. 61, no. 21, February 2014, pages 568-573 (DOI: 10.1109/TNS.2013.2292521).
29. A. Coluccia, V. Chironi, S. D'Amico "Non-idealities Compensation in Full-Digital Receivers with Application to Ultra-Wide Band" *Wireless Personal Communication*, Springer; April 2014 (DOI 10.1007/s11277-014-1777-0).
30. A. Baschiroto, M. De Matteis, A. Pezzotta, S. D'Amico "Continuous-time analog filter in CMOS nanoscale era" *Journal of Instrumentation*, IOP Publishing Ltd and Sissa Medialab srl, vol. 9, issue 4, April 2014 (DOI: 10.1088/1748-0221/9/04/C04008).
31. A. Spagnolo, B. Verbruggen, P. Wambacq, S. D'Amico "A 4.1mW 3.5GS/s 6b Time Interleaved ADC in 40nm CMOS" *IEEE Transactions on Circuits and Systems II: Express Briefs*, , vol. 61, no. 7, July 2014, pages 466 -470 (DOI: 10.1109/TCSII.2014.2327340).
32. S. D'Amico, A. Spagnolo, A. Donno, V. Chironi, P. Wambacq, A. Baschiroto "A Low-Power Analog Baseband Section for 60-GHz Receivers in 90-nm CMOS" *IEEE Transaction on Microwave Theory and Techniques*, 2014, Volume: 62, Issue:8, Pages:1724-1735 (DOI: 10.1109/TMTT.2014.2332877).
33. M. Pasca, S. D'Amico, A. Baschiroto "A 0.23μW, 96 mV Input Voltage DC-DC Converter for Body Sensor Nodes" *IEEE Sensors Journal*, Volume 15, Issue 10, 1 October 2015, Pages 5677-5682 (DOI: 10.1109/JSEN.2015.2442174).
34. M. De Matteis, A. Pezzotta, S. D'Amico, A. Baschiroto, "A 33 MHz 70 dB-SNR super-source-follower-based low-pass analog filter" *IEEE Journal of Solid-State Circuits*, volume 50, Issue 7, 1 July 2015, Pages 1516-1524 (DOI: 10.1109/JSSC.2015.2411626).
35. C. Veri, L. Francioso, M. Pasca, C. De Pascali, P. Siciliano, S. D'Amico "An 80mV Startup Voltage Fully Electrical DC-DC Converter for Flexible Thermoelectrical Generators" *IEEE Sensors Journal*, Volume 16, Issue 8, 15 April 2016, Pages 2735-2745 (DOI:10.1109/JSEN.2016.2520982)
36. R. Colella, M. Pasca, L. Catarinucci, L. Tarricone, S. D'Amico "High-Sensitivity CMOS RF-DC Converter in HF RFID Band" *IEEE Microwave and Wireless Components Letters*, Volume 26, Issue 9, September 2016, Pages 732-734 (DOI: 10.1109/LMWC.2016.2597212).
37. M. De Matteis, A. Pezzotta, A. Pipino, A. Baschiroto, S. D'Amico "21.5 dBm-IIP3 22.5 MHz fourth-order follow-the-Leader-feedback analogue filter" *IEE Electronics Letters*, Volume 52, Issue 13, 23 June 2016, Pages 1100-1102 (DOI: 10.1049/el.2016.0800).
38. M. Pasca, R. Colella, L. Catarinucci, L. Tarricone, S. D'Amico, A. Baschiroto "UHF front-end

- feeding RFID-based body sensor networks by exploiting the reader signal" *Radio Science*, Volume 51, Issue 5, 1 May 2016, Pages 481-489 (Invited Paper) (DOI: 10.1002/2016RS006016).
39. S. D'Amico et al. (Auger Collaboration) "Azimuthal asymmetry in the risetime of the surface detector signals of the Pierre Auger Observatory" *Physical Review D - Particles, Fields, Gravitation and Cosmology*, Volume 93, Issue 7, 7 April 2016 page 1-15 (DOI: 10.1103/PhysRevD.93.072006)
 40. S. D'Amico et al. (Auger Collaboration) "Search for correlations between the arrival directions of IceCube neutrino events and ultrahigh-energy cosmic rays detected by the Pierre Auger Observatory and the Telescope Array" *Journal of Cosmology and Astroparticle Physics* Volume 2016, Issue 1, 20 January 2016 (DOI: 10.1088/1475-7516/2016/01/037).
 41. S. D'Amico, A. Donno, M. Conta, A. Baschirotto "A 6.1 mW 7.5–10.6 GHz PLL-based frequency synthesizer for IEEE 802.15.4a UWB transceivers" *Analog Integrated Circuits and Signal Processing* Volume 88, Issue 3, 1 September 2016, Pages 383-389 (DOI: 10.1007/s10470-016-0758-5).
 42. R. Colella, M. Pasca, L. Catarinucci, L. Tarricone, S. D'Amico "RF-DC converter for HF RFID sensing applications powered by a near-field loop antenna" *Radio Science* Volume 51, Issue 7, 1 July 2016, Pages 942-950 (Invited Paper) (DOI: 10.1002/2016RS006017).
 43. M. De Matteis, F. Resta, A. Pipino, S. D'Amico, A. Baschirotto "A 28.8MHz 23dBm-IIP3 3.2mW Sallen-Key 4th-Order Filter with Out-of-Band Zeros Cancellation" *IEEE Transactions on Circuits and Systems II: Express Briefs*, Year: 2016, Pages: 1 - 5 (DOI: 10.1109/TCSII.2016.2619068).
 44. S. D'Amico et al. (Auger Collaboration) "Search for ultrarelativistic magnetic monopoles with the Pierre Auger Observatory" *Physical Review D* Volume 94, Issue 8, 3 October 2016, pages 1-12 (DOI: 10.1103/PhysRevD.94.082002)
 45. S. D'Amico et al. (Auger Collaboration) "Evidence for a mixed mass composition at the 'ankle' in the cosmic-ray spectrum" *Physics Letters, Section B: Nuclear, Elementary Particle and High-Energy Physics*, Volume 762, 10 November 2016, Pages 288-295 1-16 (DOI: 10.1016/j.physletb.2016.09.039)
 46. S. D'Amico et al. (Auger Collaboration) "Testing Hadronic Interactions at Ultrahigh Energies with Air Showers Measured by the Pierre Auger Observatory" *Physical Review Letters* 117, 192001, 31 October 2016 (DOI:10.1103/PhysRevLett.117.192001)
 47. M. De Matteis, A. Donno, S. D'Amico, A. Baschirotto "A 0.9V 3rd-Order 132MHz Single-OPAMP Analog Filter in 28nm CMOS" *IEE Electronics Letters*, pages 1-2 11 November 2016 (DOI: 10.1049/el.2016.3273)

Proceedings of International Conferences

48. S. D'Amico, A. Baschirotto "A 0.18u CMOS Digitally Tuned Filter For Telecom Receivers" *Proc. of IEEE International Symposium on Circuit and Systems, 2003, (ISCAS 2003)*, 25-28 May 2003, Bangkok, Thailand, pages 493-496 (ISBN: 0-7803-7761-3, DOI: 10.1109/ISCAS.2003.1205608).
49. S. D'Amico and A. Baschirotto "An Active-Gm-RC Continuous-Time Biquad Low-Pass Cell" *IEEE International Conference on Electronics, Circuits and Systems, 2003, (ICECS 2003)*, 14-17 December 2003, Sharjah (United Arab Emirates), pages 551-554 (ISBN: 0780381637, DOI: 10.1109/ICECS.2003.1301844).
50. G. Maruccio, P. Visconti, V. Arima, S. D'Amico, A. Biasco, E. D'Amone, R. Cingolani, R. Rinaldi, S. Masiero, T. Giorgi, G. Gottarelli "Hybrid molecular electronic (HME) transistor based on deoxyguanosine derivatives" *Organic Field Effect Transistors II, part of Optical Science and Technology SPIE's (48th SPIE annual meeting)*, Volume 5217, pp. 176-180, 2003, San Diego, United States (DOI: 10.1117/12.506824).
51. G. Matarrese, S. D'Amico, C. Marzocca, A. Di Giandomenico, A. Baschirotto, F. Corsi "Pseudo-random Sequence Based Tuning System for Continuous-time Filters" *Design and Automatic Test in Europe, 2004, (DATE '04)*, Paris, France, (ISBN:0-7695-2085-5, DOI: 10.1109/DATE.2004.1268833).
52. S. D'Amico, A. Baschirotto "A 0.13µm CMOS low-voltage reconfigurable filter for UMTS/WLAN transmitters" *IEEE Workshop on Wireless Circuits and Systems, 2004, (WOWCAS 2004)*, Vancouver, Canada, pages 12-13 (ISBN: 0-88865-806-0).
53. S. D'Amico and A. Baschirotto "Low-Power Compact Gm-C filters Structures" *IEEE International Symposium on Circuit and Systems, 2004, (ISCAS 2004)*, Vancouver, Canada, pages 873-876 (ISBN: 0-7803-8251-X, DOI: 10.1109/ISCAS.2004.1328334).
54. S. D'Amico, V. Giannini and A. Baschirotto "An algorithm for the automatic design and the power optimisation of Active-Gm-RC continuous-time biquadratic cells" *IEEE Northeast Workshop on Circuits and Systems, 2004, (NEWCAS '04)*, Montreal, Canada, pages 333-336 (ISBN: 0-7803-8322-2, DOI: 10.1109/NEWCAS.2004.1359099).
55. S. D'Amico, A. Baschirotto, A. Vigna, N. Ghittori, P. Malcovati "Low-power reconfigurable baseband block for UMTS/WLAN transmitters" *Norchip Conference 2004, 2004, (Norchip 2004)*, Oslo (Norway), pages 103-106 (ISBN: 0-7803-8510-1, DOI: 10.1109/NORCHP.2004.1423833).

56. S. D'Amico, V. Giannini, A. Baschirotto "A low-power reconfigurable analog filter for UMTS/WLAN receivers" Norchip Conference 2004, 2004, (Norchip 2004), Oslo (Norway), pages 179-182 (ISBN: 0-7803-8510-1, DOI: 10.1109/NORCHP.2004.1423852).
57. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico and A. Baschirotto "Behavioral analysis and dimensioning of UMTS transmitters baseband blocks" IEEE International Symposium on Circuit and Systems, 2005, (ISCAS 2005), Kobe, Japan, pages 338-391 (ISBN: 0-7803-8834-8, DOI: 10.1109/ISCAS.2005.1464606).
58. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico and A. Baschirotto "A low-distortion 1.2 V DAC+Filter for transmitters in wireless applications" IEEE International Symposium on Circuit and Systems, 2005, (ISCAS 2005), Kobe, Japan, pages 776-779 (ISBN: 0-7803-8834-8, DOI: 10.1109/ISCAS.2005.1464703).
59. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto "A low-power, low-voltage (11mW/8.4mW, 1.2V) DAC+filter for multistandard (WLAN/UMTS) transmitters" 2005 Symposium on VLSI Circuits - Digest of technical papers, 2005, (VLSI 2005), Kyoto, Japan, pages 334-337 (ISBN: 4-900784-01-X, DOI: 10.1109/VLSIC.2005.1469399).
60. S. D'Amico, T. Grassi, J. Ryckaert, A. Baschirotto "A up to 1GHz low-power continuous-time '3rd-order filter+integrator' chain for Wireless Body-Area Network receivers" Ph. D. Research in MicroElectronics and Electronics, 2005, (PRIME 2005), Lausanne, Switzerland, pages 17-20 (ISBN: 0-7803-9345-7).
61. S. D'Amico, V. Chironi, A. Baschirotto "A 0.13 μ m CMOS VGA for Multistandard Receivers" Ph. D. Research in MicroElectronics and Electronics, 2005, (PRIME 2005), Lausanne, Switzerland, pages 131-134 (ISBN: 0-7803-9345-7, DOI: 10.1109/RME.2005.1543027).
62. M. De Matteis, S. D'Amico, A. Baschirotto "A 600mV 1.32mW 75dB-DR 4th-order Baseband Analog Filter for UMTS Receivers" Ph. D. Research in MicroElectronics and Electronics, 2005, (PRIME 2005), Lausanne (Switzerland), pages 123-126 (ISBN: 0-7803-9345-7, DOI: 10.1109/RME.2005.1543025).
63. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto "A low-voltage, low-distortion (1.2V, 29.5dBm OIP3) reconfigurable baseband block for mobile applications" Ph. D. Research in MicroElectronics and Electronics, 2005, (PRIME 2005), Lausanne (Switzerland), pages 25-28 (ISBN: 0-7803-9345-7, DOI: 10.1109/RME.2005.1543000).
64. G. Matarrese, C. Marzocca, S. D'Amico, F. Corsi, A. Baschirotto "On chip self-tuning of high performance filters via pseudo-random input test signal" Proc. of IEEE 11th International Mixed Signal Testing Workshop, 2005, (IMSTW 2005), Cannes (France), pages 342-348.
65. S. D'Amico, V. Giannini, A. Baschirotto "A 1.2V-21dBm OIP₃ 4th-order Active-G_m-RC reconfigurable (UMTS/WLAN) filter with on-chip tuning designed with an automatic tool" Proc. of 31st European Solid State Circuits Conference, 2005, (ESSCIRC 2005), Grenoble, France, pages 315-318 (ISBN: 0-7803-9205-1, DOI: 10.1109/ESSCIR.2005.1541623).
66. N. Ghittori, A. Vigna, Piero Malcovati, S. D'Amico "Design of a low-power variable gain amplifier for reconfigurable wireless receivers" IEEE International Conference on Electronics, Circuits and Systems, 2005, (ICECS 2005), Gammarth (Tunisia), pages 394-397 (ISBN: 978-9972-61-100-1, DOI: 10.1109/ICECS.2005.4633461).
67. S. D'Amico, M. Conta, A. Baschirotto "A 4.1mW 79dB-DR 4th-order Source-Follower Based Continuous-Time Filter for WLAN Receivers" 2006 IEEE International Solid-State Circuits Conference - Digest of Technical Papers, 2006, (ISSCC 2006), San Francisco (United States), pages 14-15 (ISBN: 1-4244-0079-1, DOI: 10.1109/ISSCC.2006.1696187).
68. J. Ryckaert, M. Badaroglu, V. De Heyn, G. Van der Plas, P. Nuzzo, A. Baschirotto, S. D'Amico, C. Desset, H. Suys, M. Libois, B. Van Poucke, P. Wambacq, B. Gyselinckx "A 16mA UWB 3-to-5GHz 20Mpulses/s quadrature analog correlation receiver in 0.18 μ m CMOS" 2006 IEEE International Solid-State Circuits Conference - Digest of Technical Papers, 2006, (ISSCC 2006), San Francisco (United States), pages 114-115 (ISBN: 1-4244-0079-1, DOI: 10.1109/ISSCC.2006.1696068).
69. V. Giannini, P. Nuzzo, F. De Bernardinis, J. Craninckx, B. Come, S. D'Amico, A. Baschirotto "A Synthesis Tool for Power Efficient Base-Band Filter Design" Design and Automatic Test in Europe, 2006, (DATE '06), Munich (Germany), (ISBN: 3-9810801-1-4, DOI: 10.1109/DATE.2006.244059).
70. V. Giannini, J. Craninckx, J. Compriet, B. Come, S. D'Amico, A. Baschirotto "Fully reconfigurable Active-G_m-RC biquadratic cells for Software Defined Radio applications" IEEE International Symposium on Circuit and Systems, 2006, (ISCAS 2006), Kos, Greece, (ISBN: 0-7803-9389-9, DOI: 10.1109/ISCAS.2006.1692768).
71. N. Ghittori, A. Vigna, Piero Malcovati, S. D'Amico, A. Baschirotto "Analog Baseband Channel for GSM/UMTS/WLAN/Bluetooth Reconfigurable Multistandard Terminals" IEEE International Symposium on Circuit and Systems, 2006, (ISCAS 2006), Kos, Greece (ISBN: 0-7803-9389-9, DOI: 10.1109/ISCAS.2006.1693580).
72. N. Ghittori, A. Vigna, V. Ferragina, P. Malcovati, S. D'Amico, A. Baschirotto "Design of a Low-Power

- DAC System for WLAN 802.11 Wireless Transmitters" Proc. of 2006 Second International Symposium on Communications, Control and Signal Processing, 2006, (ISCCSP 2006), Marrakech, Morocco, (ISBN: 2-908849-17-8).
73. N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto "Analog Baseband Channel for Reconfigurable Multistandard (GSM/UMTS/WLAN/Bluetooth) Receivers" Wireless Reconfigurable Terminals and Platforms, 2006, (WiRTeP), Rome (Italy).
 74. Vigna, N. Ghittori, P. Malcovati, S. D'Amico, A. Baschirotto "Low-voltage baseband architectures for multi-standard wireless transmitters" Wireless Reconfigurable Terminals and Platforms, 2006, (WiRTeP), Rome (Italy).
 75. M. Presicce, M. De Matteis, S. D'Amico, D. Giotta, R. Gaggl, A. Baschirotto "A single-ended-to-differential transimpedance amplifier for a voice transceiver analog interface" Ph. D. Research in MicroElectronics and Electronics, 2006, (PRIME 2006), Otranto (Italy), pages 169-172 (ISBN: 1-4244-0157-7, DOI: 10.1109/RME.2006.1689923).
 76. M. De Matteis, S. D'Amico, A. Baschirotto "Power-minimization Design Procedure for Rauch Biquadratic Cells" Ph. D. Research in MicroElectronics and Electronics, 2006, (PRIME 2006), Otranto (Italy), pages 141-144 (ISBN: 1-4244-0157-7, DOI: 10.1109/RME.2006.1689916).
 77. S. D'Amico, M. De Blasi, M. De Matteis, V. Giannini, A. Baschirotto "A Programmable Double Supply Filter&Drive Block for Telecommunication Applications" Ph. D. Research in MicroElectronics and Electronics, 2006, (PRIME 2006), Otranto (Italy), pages 21-24 (ISBN: 1-4244-0157-7, DOI: 10.1109/RME.2006.1689886).
 78. M. De Matteis, S. D'Amico, A. Di Giandomenico, J. Hauptman, A. Baschirotto "A 600mV Baseband Analog Filter for UMTS Receivers" Ph. D. Research in MicroElectronics and Electronics, 2006, (PRIME 2006), Otranto (Italy), pages 13-16 (ISBN: 1-4244-0157-7, DOI: 10.1109/RME.2006.1689884).
 79. V. Giannini, J. Craninckx, J. Compriet, B. Come, S. D'Amico, A. Baschirotto "Flexible baseband Low-Pass Filter and Variable Gain Amplifier for Software Defined Radio Front End" Proc. of 32nd European Solid-State Circuits Conference, 2006, (ESSCIRC 2006), Montreaux (Switzerland) pages 203-206 (ISBN: 1-4244-0303-0, DOI: 10.1109/ESSCIR.2006.307566).
 80. S. D'Amico, M. De Matteis, A. Baschirotto "A 6.4mW, 4.9nV/ $\sqrt{\text{Hz}}$, 24dBm IIP3 VGA for a multi-standard (WLAN, UMTS, GSM, and Bluetooth) receiver" Proc. of 32nd European Solid-State Circuits Conference, 2006, (ESSCIRC 2006), Montreaux, Switzerland, pages 82-85 (ISBN: 1-4244-0303-0, DOI: 10.1109/ESSCIR.2006.307536).
 81. S. D'Amico, J. Ryckaert, A. Baschirotto "A up-to-1GHz low-power baseband chain for UWB receivers" Proc. of 32nd European Solid-State Circuits Conference, 2006, (ESSCIRC 2006), Montreaux (Switzerland), (ISBN: 1-4244-0303-0, DOI: 10.1109/ESSCIR.2006.307581).
 82. Nicola Ghittori, Andrea Vigna, Piero Malcovati, Stefano D'Amico, Andrea Baschirotto "A 1.2-V, 600-MS/s, 2.4-mW DAC for WLAN 802.11 and 802.16 Wireless Transmitters" Proc. of 32nd European Solid-State Circuits Conference, 2006, (ESSCIRC 2006), Montreaux, Switzerland, pages 404-407 (ISBN: 1-4244-0303-0, DOI: 10.1109/ESSCIR.2006.307616).
 83. G. Matarrese, C. Marzocca, F. Corsi, S. D'Amico, A. Baschirotto "Experimental Validation of a Tuning Algorithm for High-Speed Filters" Design and Automatic Test in Europe 2007, (DATE 2007), Munich (Germany), pages 421-426 (ISBN: 978-3-9810801-2-4, DOI: 10.1109/DATE.2007.364628).
 84. M. De Matteis, S. D'Amico, A. Baschirotto "A 600mV 3.6mW 68dB DR 4th Order Analog Base Band Filter for WLAN Receivers" IEEE International Symposium on Circuit and Systems, 2007, ISCAS 2007, New Orleans (USA), pages 3542-3545 (ISBN: 1-4244-0920-9, DOI: 10.1109/ISCAS.2007.378447).
 85. T. Fu, G. Dolmans, O. Rousseaux, L. Huang, B. Gyselinckx, J. Ryckaert, S. D'Amico, A. Baschirotto "Radio Design for Ultra Low-Power and Ultra-Wideband Wireless Sensor Nodes" 4th European Conference on Wireless Sensor Networks 2007 (EWSN 2007), Delft (Netherlands), proceedings titled "Wireless Sensors Networks: Lectures Notes on Computers Sciences" (ISBN-13: 978-3-540-69829-6) edited by Springer, Berlin (Germany), 2007, pages 27-28.
 86. P. Delizia, S. D'Amico, A. Baschirotto "A Readout Circuit in 0.35 μm CMOS Technology for Lab-on-a-Chip Applications" 2nd International Workshop on Advances in Sensors and Interfarcers 2007, (IWASI 2007), Bari (Italy), (ISBN: 978-1-4244-1245-7, DOI: 10.1109/IWASI.2007.4420009).
 87. A. Baschirotto, S. D'Amico, M. De Matteis, F. Grancagnolo, M. Panareo, R. Perrino, G. Chiodini, G. Tassielli "A CMOS high-speed front-end for cluster counting techniques in ionization detectors" 2nd International Workshop on Advances in Sensors and Interfarcers 2007, (IWASI 2007), Bari (Italy) (ISBN: 978-1-4244-1245-7, DOI: 10.1109/IWASI.2007.4420027).
 88. M. De Matteis, S. D'Amico, V. Giannini, A. Baschirotto "A 550mV 8dBm IIP3 4th order Analog Base Band Filter for WLAN Receivers" 33rd European Solid-State Circuits Conference 2007 (ESSCIRC 2007), Munich (Germany), pages 504-507 (ISBN: 978-1-4244-1125-2, DOI: 10.1109/ESSCIRC.2007.4430352).
 89. S. D'Amico, A. Baschirotto, K. Philips, J. Van der Tang, G. Dolmans, T. Fu, O. Rousseaux, H. Pflug,

- B. Gyselinckx "RF and Base-Band circuit blocks for LR-UWB receivers" International Conference on Ultra-WideBand 2007 (ICUWB2007) 24-26 September 2007 Singapore, Cina, pages 158 - 161 (ISBN: 978-1-4244-0521-3, DOI: 10.1109/ICUWB.2007.4380933).
90. S. D'Amico, M. De Matteis, A. Baschirotto, N. Ghittori, A. Vigna, P. Malcovati "A 5nV/ $\sqrt{\text{Hz}}$ -IRN, 78dB-Gain-Range, 78dB-DR Multi-Standard Baseband Chain for Bluetooth, UMTS and WLAN" 33rd European Solid-State Circuits Conference 2007 (ESSCIRC 2007), 11-13 September 2007, Munich (Germany) pages 441-444 (ISBN: 978-1-4244-1125-2, DOI: 10.1109/ESSCIRC.2007.4430337).
 91. G. Dolmans, O. Rousseaux, Li Huang, Ting Fu, B. Gyselinckx, S. D'Amico, A. Baschirotto, J. Ryckaert, B. van Poucke "UWB Radio Transceivers For Ultra Low Power and Low Data Rate Communications" International Conference on Ultra-WideBand, 2007, (ICUWB2007), 24-26 September 2007, Singapore, Cina, pages 152-155 (ISBN: 978-1-4244-0521-3, DOI: 10.1109/ICUWB.2007.4380932)
 92. S. D'Amico, M. De Matteis, A. Baschirotto "A 6th-order 100 μ A 280MHz Source-Follower-Based Single-loop Continuous-Time Filter", 2008 International Solid-State Circuits Conference (ISSCC 2008)-Digest of Technical Papers, 2008, San Francisco, USA, pages 12-14 (ISBN: 978-1-4244-2010-0, DOI: 10.1109/ISSCC.2008.4523062).
 93. M. De Matteis, S. D'Amico, A. Baschirotto "Advanced Analog Filters for Telecommunications", Proc. of Design, Automation and Test in Europe (DATE 2008), 2008, Munich, Germany, pages 1316-1321, (ISBN: 978-3-9810801-3-1, DOI: 10.1109/DATE.2008.4484861).
 94. P. Delizia, M. De Matteis, S. D'Amico, A. Baschirotto, C. Azeredo-Leme, Ricardo Reis "Design Procedure for DVB-T Receivers Large Tuning Range LP Filter" Proc. of IEEE International Symposium on Circuit and Systems (ISCAS 2008), 2008, Seattle, USA, pages 2913-2916 (ISBN: 978-1-4244-1683-7, DOI: 10.1109/ISCAS.2008.4542067).
 95. Cito, M. De Matteis, S. D'Amico, A. Baschirotto, P. Delizia, R. Reis, W. Zhao, C. Azeredo-Leme, A. Tavares "A CMOS 90nm 4-mW 15dBm-IIP3 Base-Band Programmable Gain Amplifier for UWB Receivers", Ph. D. Research in MicroElectronics and Electronics (PRIME 2008), 2008, Istanbul (Turkey), pages 213-216 (ISBN: 978-1-4244-1983-8, DOI: 10.1109/RME.2008.4595763).
 96. M. De Matteis, S. D'Amico, A. Baschirotto, P. Delizia, C. Azeredo-Leme, A. Tavares, "A 90nm-CMOS 1.8mW 87dB-SNR 3rd Order Analog Filter for GSM Receivers" Ph. D. Research in MicroElectronics and Electronics (PRIME 2008), 2008, Istanbul (Turkey), pages 173-176, (ISBN: 978-1-4244-1983-8, DOI: 10.1109/RME.2008.4595753).
 97. P. Delizia, S. D'Amico, A. Baschirotto "A 150 μ W-11b Readout Circuit for Lab-on-a-Chip Applications" Ph. D. Research in MicroElectronics and Electronics (PRIME 2008), 2008, Istanbul (Turkey) pages 193-196 (ISBN: 978-1-4244-1983-8, DOI: 10.1109/RME.2008.4595758).
 98. De Matteis, M., D'Amico, S., Baschirotto, A., Delizia, P., Reis, R., Wen-Hu Zhao, Azeredo-Leme, C., Tavares, A. "A CMOS-90nm 15.6mW 20dBm-in-band-IIP3 analog baseband chain for UWB receivers" Ph. D. Research in MicroElectronics and Electronics (PRIME 2008), 2008, Istanbul, Turkey, pages 45-48, (ISBN: 978-1-4244-1983-8, DOI: 10.1109/RME.2008.4595721).
 99. P. Delizia, S. D'Amico, A. Baschirotto, M. Scandiuozzo, M. Sergio, N. Manaresi, G. Medoro "A 640 \times 480 Active Chip for Cell Detection with 11b Digital Output", IEEE Sensors, 2008, Lecce, Italy, pages 1265-1268 (ISBN: 978-1-4244-2580-8, DOI: 10.1109/ICSENS.2008.4716674).
 100. S. D'Amico, A. Baschirotto, K. Philips, O. Rousseaux, B. Gyselinckx "A 240MHz Programmable Gain Amplifier & Filter for Ultra Low Power Low-Rate UWB receivers" Proc. of 35th European Solid-State Circuits Conference, (ESSCIRC 2009), 2009, Athens, Greece, pages 260-263 (ISBN: 978-1-4244-4354-3, DOI: 10.1109/ESSCIRC.2009.5326006).
 101. A. Baschirotto, V. Chironi, G. Cocciolo, S. D'Amico, M. De Matteis, P. Delizia "Low Power Analog Design in Scaled Technologies" Topical Workshop on Electronics for Particles Physics 2009 (TWEPP), 2009, Paris, France, pages 103-109 (ISBN: 9789290833352).
 102. S. D'Amico, M. De Matteis, F. Grancagnolo, M. Panareo, R. Perrino, G. Chiodini, A. Corvaglia, G. Cocciolo, A. Baschirotto "A Low Power 0.13 μ m ADC for Drift Chambers", IEEE International Conference on Electronics, Circuits and Systems (ICECS 2009), 2009, Hammamet (Tunisy), pages 407-410 (ISBN: 978-1-4244-5090-9, DOI: 10.1109/ICECS.2009.5410904).
 103. S. D'Amico, M. De Matteis, G. Cocciolo, A. Baschirotto "A 4th Order CMOS 65nm Wideband Low Power Analog Filter for Wireless Receivers" IEEE International Conference on Electronics, Circuits and Systems (ICECS 2009), 2009, Hammamet, Tunisy, pages 191-193 (ISBN: 978-1-4244-5090-9, DOI: 10.1109/ICECS.2009.5410968).
 104. S. D'Amico, M. De Matteis, F. Grancagnolo, M. Panareo, R. Perrino, G. Chiodini, A. Corvaglia, G. Cocciolo, A. Baschirotto "A 0.13 μ m CMOS VGA for Drift Chambers" IEEE International Conference on Electronics, Circuits and Systems (ICECS 2009), 2009, Hammamet, Tunisy, pages 936-939 (ISBN: 978-1-4244-5090-9, DOI: 10.1109/ICECS.2009.5410823).
 105. P. Delizia, G. Saccomanno, S. D'Amico, A. Baschirotto "A 10-b 100-MS/s Pipelined ADC with an optimized bit-stage resolution in 65nm CMOS Technology", IEEE International Symposium on Circuits and Systems (ISCAS 2010), 2010, Paris, France, pages 4033-4036 (ISBN: 978-1-4244-

- 5308-5, DOI: 10.1109/ISCAS.2010.5537644).
106. S. D'Amico, G. Cocciolo, M. De Matteis, A. Baschirotto "A 6.9mA 5bits 90nm 1Gs/s ADC without calibration" IEEE International Analog VLSI Workshop, 2010, Pavia, Italy, pages 265-269 .
 107. G. Cocciolo, S. D'Amico, M. De Matteis, M. Panareo, A. Corvaglia, A. Baschirotto "A low power front-end drift for chambers" IEEE International Analog VLSI Workshop, 2010, Pavia, Italy, pages 243-246.
 108. M. De Matteis, S. D'Amico, G. Cocciolo, A. Baschirotto "A 65nm Minimum-Power 1.3mW 52dB-DR Filter-&Amplifier for DVB-T Receivers" IEEE International Analog VLSI Workshop, 2010, Pavia, Italy, pages 189-192.
 109. C. Zhou, P. Harpe, S. Rampu, X. Wang, S. D'Amico, A. Baschirotto, K. Philips, G. Dolmans, H. de Groot "A 56 μ W VGA with 5MHz Bandwidth and 47dB Gain-Range in 90nm CMOS" International Symposium on VLSI Design, Automation and Test (VLSI DAT) 2010, Hsinchu, TAIWAN, pages 91-94 (ISBN: 978-1-4244-5269-9, DOI: 10.1109/VDAT.2010.5496699).
 110. D'Amico, S., Baschirotto, A., De Matteis, M., Grancagno, F., Panareo, M., Perrino, R., Chiodini, G., Corvaglia, A. "A 0.13 μ m CMOS front-end for drift chambers" Proc. of AISEM 2010 titled "Lecture Notes in Electrical Engineering", 54 LNEE, pp. 295-298 published by Springer, 2010 (ISBN: 978-90-481-3605-6).
 111. S. D'Amico, A. Spagnolo, A. Donno, P. Wambacq, A. Baschirotto "A 9.5mW Analog Baseband RX Section for 60GHz Communications in 90nm CMOS" Proceeding of IEEE Radio Frequency Integrated Circuits Symposium (RFIC), 2011, 4-7 June 2011, Baltimore (USA) pages 1-4 (ISBN: 978-1-4244-8293-1, DOI: 10.1109/RFIC.2011.5940689).
 112. S. D'Amico, G. Cocciolo, M. De Matteis, A. Baschirotto "A 7.65mW 5bits 90nm 1Gs/s ADC folded-interpolated without calibration" Proceeding of ESSCIRC 2011, 12-16 September 2011, Helsinki (Finland), pages 151-154 (ISBN: 978-1-4577-0703-2, DOI: 10.1109/ESSCIRC.2011.6044887).
 113. Xang, K. Philips, C. Zhou, B. Busze, P. Harpe, H. Pflug, A. Young, J. Romme, S. D'Amico, M. De Matteis, A. Baschirotto, H. De Groot "A High-Band IR-UWB Chipset for Real-Time Duty-Cycled Communication and Localization Systems" Proceeding of IEEE Asian Solid-State Circuits Conference 2011 (A-SSCC 2011), 14-16 November 2011, Jeju (Korea), pages 381-384 (ISBN: 978-1-4577-1784-0, DOI: 10.1109/ASSCC.2011.6123595).
 114. Costantini, G. Cavalera, A. Pepino, M. De Matteis, G. Cocciolo, M. De Blasi, S. D'Amico, P. Visconti, A. Baschirotto "A CMOS low-power SoC for HID and LED lamps ballast" Proceeding of Ph.D. Research in Microelectronics and Electronics (PRIME), 2011, Trento (Italy), pages 109 – 112 (ISBN: 978-1-4244-9138-4, DOI: 10.1109/PRIME.2011.5966229).
 115. M. De Matteis, G. Cocciolo, S. D'Amico, A. Baschirotto, M. Sabatini "A 115 μ W UWB Programmable Gain Amplifier for Intelligent Tire Personal Area Network" Proc. of IEEE International Symposium on Circuits and Systems 2012, (ISCAS 2012), 20-23 May 2012 Seoul, Korea, pages 1500-1503 (ISBN: 978-1-4673-0218-0, DOI: 10.1109/ISCAS.2012.6271533).
 116. K. Raczkowski, G. Mangraviti, V. Szortyka, A. Spagnolo, B. Parvais, R. Vandebriel, V. Vidojkovic, C Soens, S. D'Amico, P. Wambacq "A four-path 60 GHz phased-array receiver with injection-locked LO, hybrid beamforming and analog baseband section in 90 nm CMOS" Proc. of Proceeding of IEEE Radio Frequency Integrated Circuits Symposium (RFIC), 2012, 17-19 June 2012, Montreal, Canada, pages 431-434 (ISBN: 978-1-4673-0413-9, DOI: 10.1109/RFIC.2012.6242315).
 117. M. De Matteis, S. D'Amico, A. Costantini, A. Pezzotta, A. Baschirotto "A 1.25mW 3rd-order Active-Gm-RC 250MHz-bandwidth analog filter based on power-stability optimization" 2012 19th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2012, December 9-12 2012, Seville, Spain pages 260-263.
 118. A. Costantini, A. Pezzotta, A. Baschirotto, M. De Matteis, S. D'Amico, F. Murtas, G. Gorini "A CMOS 0.13 μ m low power front-end for GEM detectors" 2012 19th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2012, December 9-12 2012, Seville, Spain pages 260-263.
 119. M. De Matteis, G. Cocciolo, S. D'Amico, A. Baschirotto, M. Sabatini "A 115 μ W UWB Programmable Gain Amplifier for Intelligent Tire Personal Area Network" IEEE International Symposium on Circuits and Systems (ISCAS), 2012, May 20-23 2013, Seul, Korea, pages 1500-1503 (DOI: 10.1109/ISCAS.2012.6271533).
 120. M. De Matteis, S. D'Amico, G. Cocciolo, M. De Blasi, A. Baschirotto "A 54dB-DR 1-GHz-bandwidth continuous-time low-pass filter with in-band noise reduction" IEEE International Symposium on Circuits and Systems (ISCAS), 2013, May 19-23 2013, Beijing, China, pages 180-183 (ISBN: 978-146735760-9, DOI: 10.1109/ISCAS.2013.6572087).
 121. M. De Blasi, M. Pierri, S. D'Amico, M. De Matteis, A. Baschirotto, A. Bau, A. Passerini, M. Gervasi, M. Zannoni "A Monolithic CMOS Automatic Biasing System for 40GHz Multistage HEMT" IEEE International Symposium on Circuits and Systems (ISCAS), 2013, May 19-23 2013, Beijing,

- China, pages 1708-1711 (ISBN: 978-146735760-9, DOI: 10.1109/ISCAS.2013.6572193).
122. M. De Matteis, S. D'Amico, G. Cocciolo, M. De Blasi, A. Baschirotto "A monolithic CMOS automatic biasing system for 40GHz multistage HEMT" IEEE International Symposium on Circuits and Systems (ISCAS), 2013, May 19-23 2013, Beijing, China, pages 1708-1711 (ISBN: 978-146735760-9, DOI: 10.1109/ISCAS.2013.6572193).
 123. A. Coluccia, V. Chironi, S. D'Amico "Non-idealities compensation in full-digital receivers with application to Ultra-Wide Band" 2013 International Conference on Computer, Information and Telecommunication Systems (CITS), 2013, May 7-8 2013, Athens, Greece (ISBN: 978-1-4799-0166-1, DOI: 10.1109/CITS.2013.6705724).
 124. T. Vergine, M. De Matteis, S. D'Amico, V. Chironi, A. Marchioro, K. Kloukinas, A. Baschirotto "A 32-Channel 12-bits Single Slope A-to-D Converter for LHC environment" Proc. of International Conference on IC Design and Technology (ICICDT), 2013, May 29th-31st 2013, Pavia, Italy (ISBN: 978-146734741-9, DOI: 10.1109/ICICDT.2013.6563322).
 125. A. Baschirotto, M. De Matteis, S. D'Amico "Towards Minimum Power Analog Filters" Proc. of International Conference on IC Design and Technology (ICICDT), 2013, May 29th-31st 2013, Pavia, Italy (invited) (ISBN: 978-146734741-9, DOI: 10.1109/ICICDT.2013.6563321).
 126. V. Chironi, S. D'Amico, M. De Matteis, A. Baschirotto "A Dual-Band Balun LNA Resilient to 5-6 GHz WLAN Blockers for IR-UWB in 65nm CMOS" Proc. of International Conference on IC Design and Technology (ICICDT), 2013, May 29th-31st 2013, Pavia, Italy (ISBN: 978-146734741-9, DOI: 10.1109/ICICDT.2013.6563329).
 127. A. Pezzotta, A. Costantini, M. De Matteis, S. D'Amico, G. Gorini, F. Murtas, A. Baschirotto "A Low-Power CMOS 0.13 μ m Charge-Sensitive Preamplifier for GEM Detectors" Proc. of International Conference on IC Design and Technology (ICICDT), 2013, May 29th-31st 2013, Pavia, Italy (ISBN: 978-146734741-9, DOI: 10.1109/ICICDT.2013.6563324).
 128. L. Francioso, C. De Pascali, P. Siciliano, A. De Risi, S. D'Amico, C. Veri, M. Pasca "Thin film technology flexible thermoelectric generator and dedicated ASIC for energy harvesting applications" 5th IEEE International Workshop on Advances in Sensors and Interfaces, IWASI 2013, Bari, Italy, June 13-14 2013, pages 104-107 (ISBN: 978-147990040-4, DOI: 10.1109/IWASI.2013.6576100).
 129. A. Pipino, M. De Blasi, M. De Matteis, S. D'Amico, A. Fornasari, A. Baschirotto "A 36 μ W rail-to-rail-input chopper stabilized amplifier using correlated double sampling" 9th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2013, Villach (Austria), 24-27 June 2013, pages 69-72 (ISBN: 978-146734580-4, DOI: 10.1109/PRIME.2013.6603126).
 130. M. Pasca, V. Chironi, S. D'Amico, M. De Matteis, A. Baschirotto "A 12dBm IIP3 reconfigurable mixer for high/low band IR-UWB receivers" 9th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2013, Villach (Austria), 24-27 June 2013, pages 69-72 (ISBN: 978-146734580-4, DOI: 10.1109/PRIME.2013.6603101).
 131. A. Donno, S. D'Amico, M. De Matteis, A. Baschirotto "A 10-b 100-MSPS low power pipeline ADC for high energy physics experiments" 9th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2013, Villach (Austria), 24-27 June 2013, pages 69-72 (ISBN: 978-146734580-4, DOI: 10.1109/PRIME.2013.6603152).
 132. A. Pezzotta, M. De Matteis, S. D'Amico, A. Baschirotto "A CMOS-28nm 880-MHz 4th-order low-pass active-RC filter for 60 GHz transceivers" 9th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2013, Villach (Austria), 24-27 June 2013, pages 69-72 (ISBN: 978-146734580-4, DOI: 10.1109/PRIME.2013.6603164).
 133. A. Donno, S. D'Amico, M. De Matteis, A. Baschirotto "A 23mW 4.5/8 GHz IR-UWB transmitter in 65nm TSMC CMOS technology (Conference Paper)" 9th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2013, Villach (Austria), 24-27 June 2013, pages 69-72 (ISBN: 978-146734580-4, DOI: 10.1109/PRIME.2013.6603148).
 134. Chironi, V., D'Amico S., Pasca, M., De Matteis, M., Baschirotto A. "A SAW-less dual-band RF front-end for IR-UWB receiver in 65nm CMOS" Proceedings - IEEE International Symposium on Circuits and Systems, ISCAS 2014, Melbourne, (Australia), 1-5 June 2014, pages 1909-1912 (ISBN: 978-147993432-4, DOI: 10.1109/ISCAS.2014.6865533).
 135. C. Veri, M. Pasca, S.D'Amico, L. Francioso "A 40mV Start-up Voltage DC – DC ConverterFor Thermoelectric Energy Harvesting Applications" 10th Conference on Ph. D. Research in Microelectronics and Electronics, PRIME 2014, Grenoble (France), 20 June 2014 – 3 July 2014, pages 1-4 (DOI: 10.1109/PRIME.2014.6872740).
 136. Spagnolo A., Verbruggen B., D'Amico S., Wambacq P., "A 6.2mW 7b 3.5GS/s time interleaved 2-stage pipelined ADC in 40nm CMOS" 40th European Solid State Circuits Conference (ESSCIRC), 2014, Venice (Italy), 22-26 September 2014, pages 75-78 (ISBN: 978-1-4799-5694-4, DOI: 10.1109/ESSCIRC.2014.6942025).
 137. M. De Matteis, A. Pezzotta, S. D'Amico, A. Baschirotto "A 33-MHz 70dB-SNR Super-Source-Follower-Based LowPass Analog Filter" 140th European Solid State Circuits Conference (ESSCIRC), 2014, Venice (Italy), 22-26 September 2014, pages (ISBN: 978-1-4799-5694-4, DOI:).

138. F. Resta, S. D'Amico, M. De Matteis, A. Baschiroto "An improved Source-Follower Based Sallen-Key Continuous-Time Biquadratic Cell with Auxiliary Path" Norchip Conference 2014, 27-28 October 2014, Tampere (Finland), pages 1-4 (DOI:10.1109/NORCHIP.2014.7004729).
139. V. Chironi, M. Pasca, P. Siciliano, S. D'Amico "A 5.8–13 GHz SDR RF front-end for wireless sensors network robust to out-of-band interferers in 65nm CMOS" 6th IEEE International Workshop on Advances in Sensors and Interfaces (IWASI), 18-19 June 2015, Gallipoli (Lecce), Pages: 137 - 140 (DOI: 10.1109/IWASI.2015.7184927).
140. C. Veri, M. Pasca, S. D'Amico, L. Francioso, C. De Pascali, P. Siciliano "A flexible thermoelectric generator with a fully electrical, low startup voltage and high efficiency DC-DC converter" 6th IEEE International Workshop on Advances in Sensors and Interfaces (IWASI), 18-19 June 2015, Gallipoli (Lecce), Pages: 141 - 145, (DOI: 10.1109/IWASI.2015.7184928).
141. M. Pasca, S. D'Amico, V. Chironi, L. Catarinucci, D. de Donno, R. Colella, L. Tarricone "A -19dBm sensitivity integrated RF-DC converter with regulated output voltage for powering UHF wireless sensors" 6th IEEE International Workshop on Advances in Sensors and Interfaces (IWASI), 18-19 June 2015, Gallipoli (Lecce), pages: 168 - 171 (DOI: 10.1109/IWASI.2015.7184926).
142. A. Spagnolo, B. Verbruggen, S. D'Amico, P. Wambacq "High-speed analog-to-digital converters in downscaled CMOS" International Conference on IC Design & Technology (ICICDT), 1-3 June 2015, Leuven (Belgium) pages: 1 – 4 (Invited Paper) (DOI: 10.1109/ICICDT.2015.7165903).
143. A. D'Amico, M. De Matteis, S. D'Amico, L. Crespi, A. Baschiroto "A 4th-order low-power diode-C-based filter with 12dBm-IIP3 at the cut-off frequency" 2015 IEEE International Symposium on Circuits and Systems (ISCAS), 24-27 May 2015 Lisbon (Portugal), Pages: 2656 - 2659, (DOI: 10.1109/ISCAS.2015.7169232).
144. F. Resta, M. De Matteis, A. Pezzotta, S. D'Amico, A. Baschiroto "A 30MHz 28dBm-IIP3 3.2mW Fully-Differential Sallen-Key 4th-Order Filter with Out-of-Band Zeros Cancellation" 13th IEEE International New Circuits And Systems Conference (NEWCAS 2015), 7-10 June 2015, Grenoble (France), pages 1-4 (DOI: 10.1109/NEWCAS.2015.7182066).
145. A. D'Amico, M. De Matteis, S. D'Amico, C. De Berti, L. Crespi, A. Baschiroto "A 4th-order 100µA diode-C-based filter with 5dBm-IIP3 at the 24MHz cut-off frequency" Proceedings of 41st European Solid-State Circuits Conference, ESSCIRC 2015, Pages 36-39, Graz (Austria) 14-18 September 2015 (DOI: 10.1109/ESSCIRC.2015.7313822).
146. C. Veri, M. Pasca, S. D'Amico, L. Francioso, C. De Pascali, P. Siciliano "A thin film flexible thermoelectric generator with a fully electrical, low startup voltage and high efficiency DC - DC converter" 14th IEEE SENSORS - Proceedings; Busan (South Korea), 1-4 November 2015 (DOI: 10.1109/ICSENS.2015.7370285).
147. A. Donno, S. D'Amico, M. De Matteis, A. Baschiroto "A 150MHz 3rd-order single Opamp continuous-time analog filter in 28nm CMOS technology" Proceedings of the IEEE International Conference on Electronics, Circuits, and Systems, ICECS 2015, Cairo (Egypt); 6-9 December 2015 (DOI: 10.1109/ICECS.2015.7440274).
148. A. Donno, S. D'Amico, M. Conta, A. Baschiroto "A 6.1mW 7.5-10.6 GHz PLL-Based Frequency Synthesizer for IEEE 802.15.4a in 65nm CMOS" Proceedings of the IEEE International Conference on Electronics, Circuits, and Systems, ICECS 2015, Cairo (Egypt); 6-9 December 2015.
149. M. Pasca, L. Catarinucci, R. Colella, D. De Donno, L. Tarricone, S. D'Amico "A HF-RFID, -19 dBm sensitivity fully integrated RF-DC voltage multiplier" 15th IEEE Mediterranean Microwave Symposium, MMS 2015, Lecce (Italy); 30 November-2 December 2015 (DOI: 10.1109/MMS.2015.7375374).
150. M. Pasca, L. Catarinucci, R. Colella, D. De Donno, L. Tarricone, S. D'Amico, A. Baschiroto "A UHF-RFID power management circuit for body sensor networks" 15th IEEE Mediterranean Microwave Symposium, MMS 2015, Lecce (Italy); 30 November-2 December 2015 (DOI: 10.1109/MMS.2015.7375373).
151. M. De Matteis, F. Resta, A. Pipino, S. D'Amico, A. Baschiroto "A 28.8MHz 21.1dBm-IIP3 3.2mW Sallen-Key 4th-Order filter with out-of-band zeros cancellation" Proceedings of IEEE International Symposium on Circuits and Systems, ISCAS 2016, Montreal (Canada) May 22-25, 2016, Pages 2902-2905 (DOI: 10.1109/ISCAS.2016.7539205)
152. A. Pipino, M. De Matteis, A. Pezzotta, F. Resta, S. D'Amico, A. Baschiroto "A 22.5MHz 21.5dBm-IIP3 4th-Order FLFB analog filter" Proceedings of the 42nd European Solid-State Circuits Conference, ESSCIRC 2016, Lausanne (Switzerland), 12-15 Sept. 2016, pages 289-292 (DOI: 10.1109/ESSCIRC.2016.7598299).

Books chapters

153. R. Rinaldi, G. Maruccio, A. Bramanti, P. Visconti, A. Biasco, V. Arima, S. D'Amico, R. Cingolani "Nano-Bio Electronic Devices Based on DNA Bases and Proteins" a chapter of the book titled "Frontiers of Multifunctional Integrated Nanosystems" NATO Science Series, 2005, Volume

- 152, Part VI, edited by Eugenia Buzaneva and Peter Scharff, published by: Kluwer Academic Publisher (Dordrecht, the Netherlands), 1st August 2004, pages 225-250 (ISBN of the book: 978-1402021725, DOI of the book chapter: 10.1007/1-4020-2173-9_22).
154. R. Rinaldi, G. Maruccio, A. Biasco, P.P. Pompa, A. Bramanti, V. Arima, S. D'Amico, E. D'Amone, and R. Cingolani "Metalloprotein-based electronic nanodevices" a chapter of the book titled "Bionanotechnology: Proteins to Nanodevices" edited by V. Renugopalakrishnan and Randolph V. Lewis, pages 9-13, published by Springer (Dordrecht, the Netherlands), 2006 (ISBN-10: 1402042191, ISBN-13: 978-1402042195, DOI: 10.1007/978-1-4020-4375-8_2).
 155. A. Baschiroto, S. D'Amico, P. Malcovati "Low-Voltage, Low-Power Basic Circuits" a chapter of the book titled "Analog Circuit Design" edited by M. Steyert, A. H.M. van Roermund, J. H. Huijsing, pages 291-329, Springer (the Netherlands), 2006 (ISBN: 978-1-4020-3884-6 (Print) 978-1-4020-3885-3 (Online), DOI: 10.1007/1-4020-3885-2_14).
 156. A. Baschiroto, S. D'Amico, M. De Matteis "Advances on Analog Filters for Telecommunications" a chapter of the book titled "Advanced Signal Processing, Circuits, and System Design Techniques for Communications" edited by V. Paliouras, T. Stouraitis, and A. Ioinovici, pages 131-168, published by IEEE (New York), 2006 (ISBN 10: 1424404606 / ISBN 13: 9781424404605, DOI: 10.1109/ASPCAS.2006.251117).
 157. S. D'Amico, M. De Matteis, O. Rousseaux, K. Philips, B. Gyselinck, D. Neiryneck, A. Baschiroto "Ultra Wide Band in Medical Applications" a chapter of the book titled "Advances in Biomedical Sensing, Measurements, Instrumentation and Systems", edited by Mukhopadhyay Subhas Chandra, Lay-Ekuakille Aimé, published by Springer (Dordrecht, the Netherlands) in 2010 (ISBN: 978-3-642-05166-1, DOI: 10.1007/978-3-642-05167-8_4).
 158. S. D'Amico, M. De Matteis, A. Baschiroto. "Source-Follower-Based Continuous Time Analog Filters" caphter 9 of the book titled "Analog Circuit Design" page(s):167 – 187, edited by Roermund, Arthur H. M. van, Casier Herman; Steyaert Michael, published by Springer (Dordrecht, the Netherlands), 2010 (ISBN: 978-90-481-3082-5, DOI: 10.1007/978-90-481-3083-2_9).
 159. M. De Matteis, S. D'Amico, A. Baschiroto "Filtro Analogico Passa-Basso a singolo OPAMP" a chapter of the book titled " Elettronica Analogica", edited by F. Corsi, pages 167-194, 2011, published by Mc Graw Hill (Milan, Italy) (ISBN: 9788838665677).

Patents

160. G. Matarrese, S. D'Amico, C. Marzocca, A. Di Giandomenico, A. Baschiroto, F. Corsi "Method and Apparatus for Tuning a Filter" US Patent, Patent no. US6965275, November 15th 2005, USA.
161. S. D'Amico, A. Baschiroto, A. Gola "Continuous Time Filter" US Patent, Patent no. US7026856, April 11th 2006, USA.
162. S. D'Amico, M. Conta, A. Baschiroto "Filter Circuit" US Patent, Patent no. US7659774, 9th of February 2010, USA.
163. S. D'Amico, M. Pasca, F. Dalena "Low power DC-DC converter" International Patent, PCT application no. PCT/GB2005/051534, 27th of May 2015, USA.